



Dudeck 3-6-11-34-7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Dudeck et al.
Case: 3-6-11-34-7
Serial No.: 10/764,000
Filing Date: January 23, 2004
Group: 2827
Examiner: Son L. Mai

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature: *Son L. Mai* Date: February 28, 2006

Title: Method and Apparatus for Reducing Leakage Current in a Read Only Memory Device Using Transistor Bias

AMENDMENT AFTER FINAL REJECTION
UNDER 37 CFR §1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the outstanding final Office Action, dated January 4, 2006, please amend the above-identified patent application as follows:

Entry is approved
f-12 03/14/06